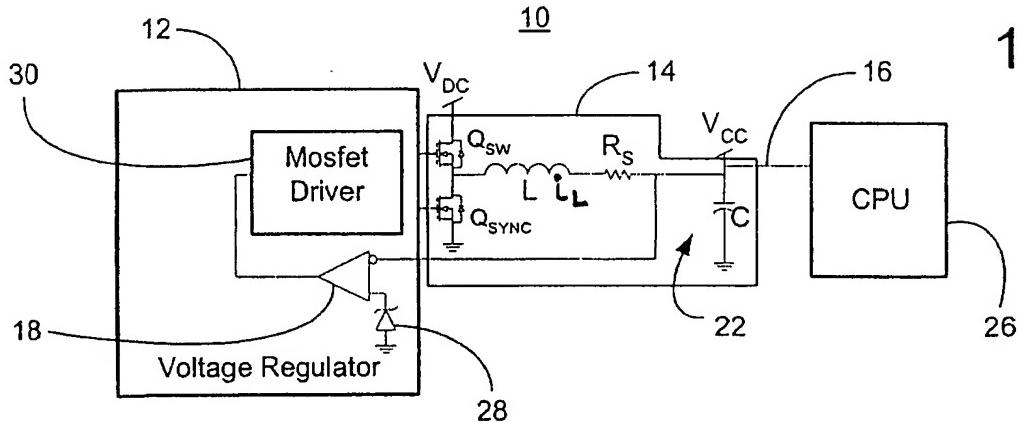


CPU SURGE REDUCTION AND PROTECTION  
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PRIOR ART  
 FIG. 1

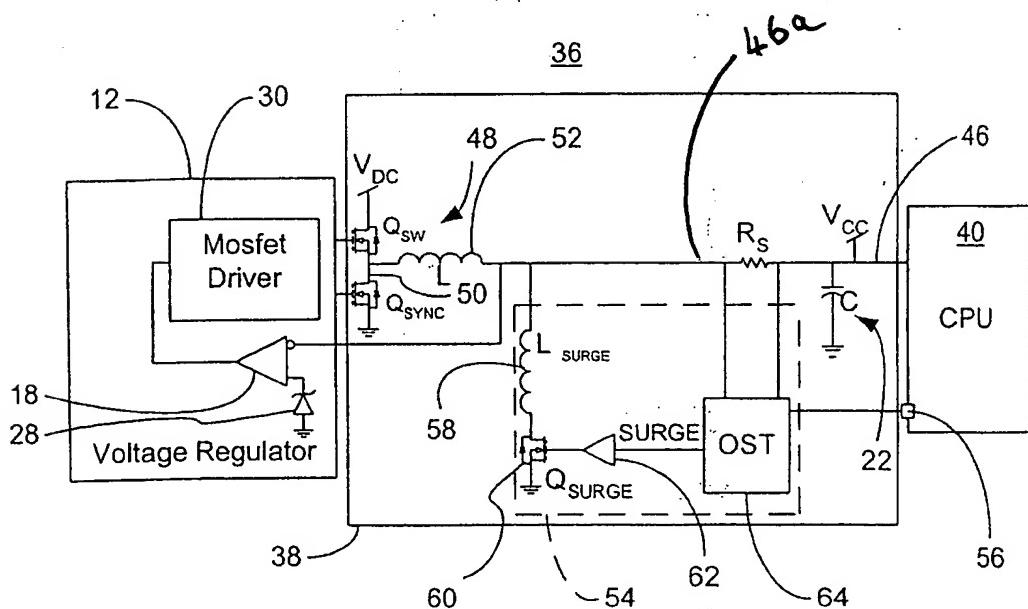
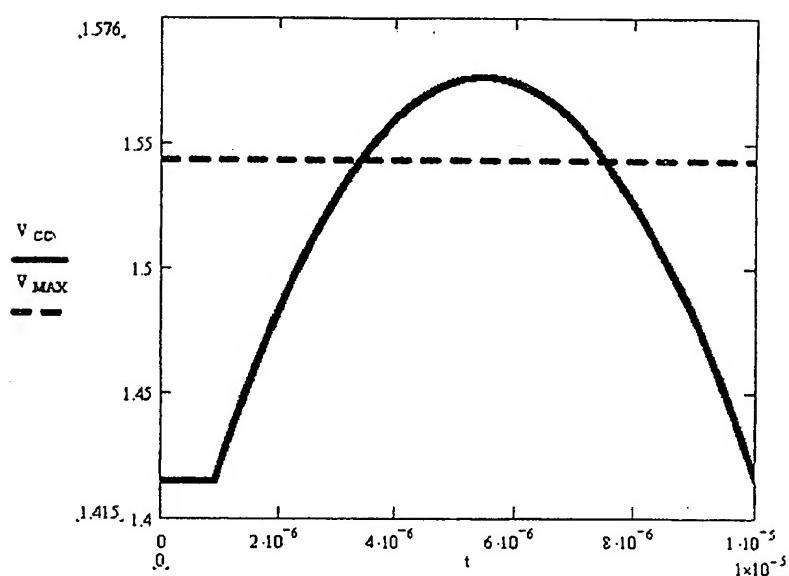


FIG. 4A

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PRIOR ART  
FIG. 2

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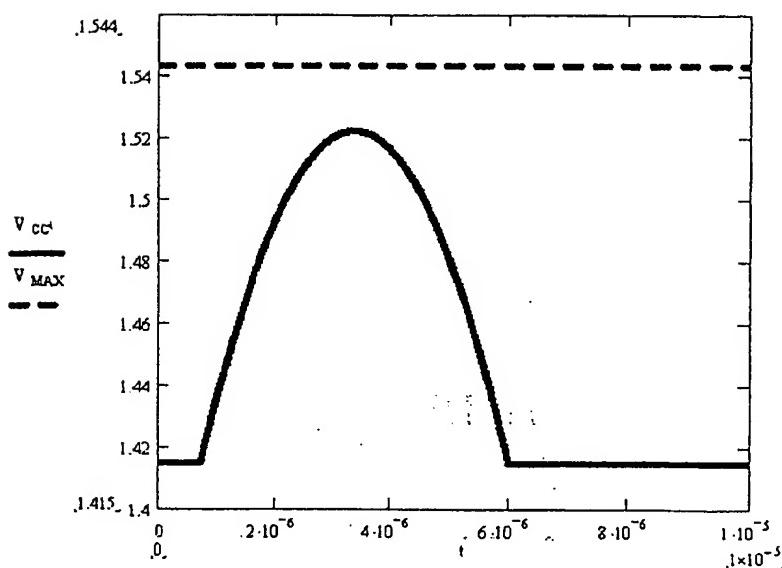


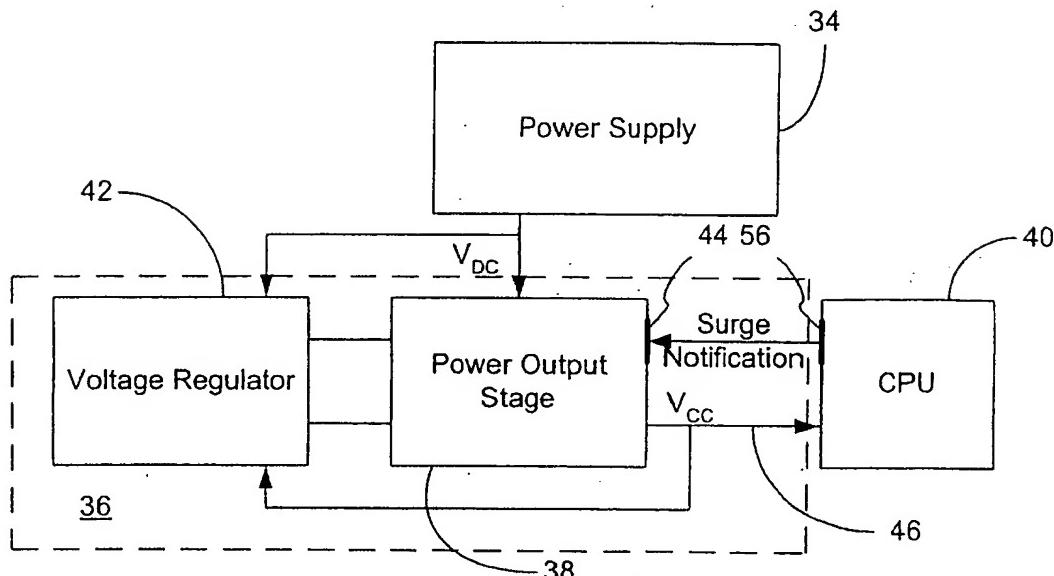
FIG. 5



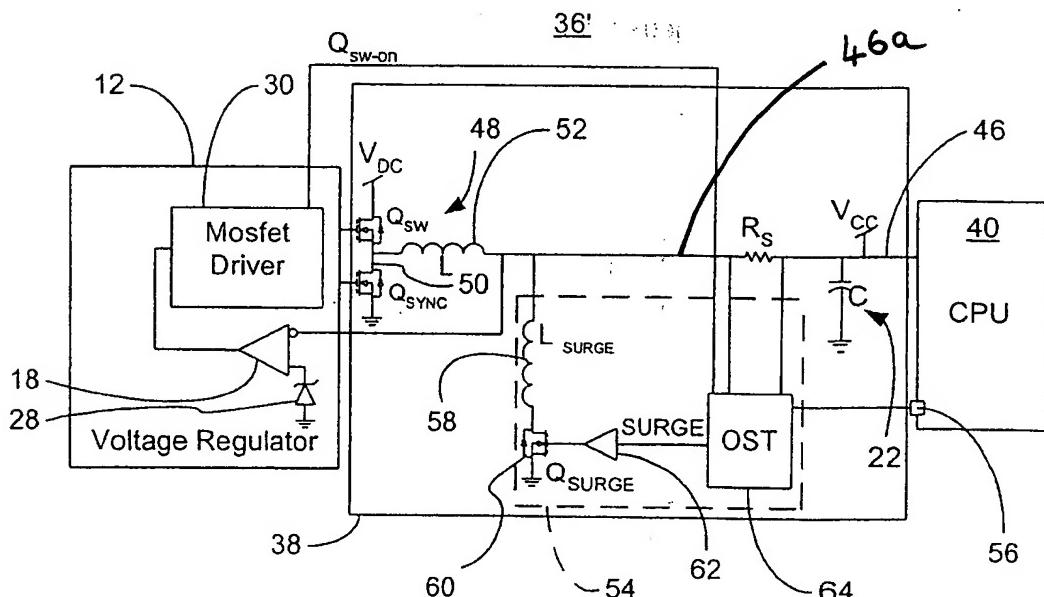
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**FIG. 3**



**FIG. 4B**